

ABSTRACT OF DISCLOSURE

A command fetch control section 1 has an address selection function of selecting the start address of an initialization program stored in a local memory (RAM 6) or an external memory (ROM 9) connected through external buses 7 and 8, based on an address selection signal MS externally given. When a processor 10 in which one or both of the supplies of its internal clocks bck and cck have been stopped after the system was powered on, is to be restarted, the processor 10 can read out the initialization program necessary for its restarting operation from the high-speed RAM 6. Thus the restarting operation can be performed rapidly.